PATENT NUMBER and ISSUE DATE U.S. UTILITY Patent Application **APPL NUM** FILING DATE **CLASS** SUBCLASS GAU **EXAM:NER** 10078732 02/19/2002 716 2825 *APPLICANTS: Tseng Kenneth; **CONTINUING DATA VERIFIED: BO ** FOREIGN APPLICATIONS VERIFIED: PG-PUB DO NOT PUBLISH 🖵 RESCIND -□ yes 🖢 no ATTORNEY DOCKET NO Foreign priority claimed □ yes 🗹 no 35 USC 119 conditions met 07716P001 Verified and Acknowledged Examiners's intials TITLE: Variable stage ratio buffer insertion for noise optimization in a logic network NOTICE OF ALLOWANCE MAILED CLAIMS ALLOWED Assistant Examiner ISSUE FEE DRAWING Date Paid **Amount Due** Sheets Drwg. Figs.Drwg. **Primary Examiner** Application Examiner TERMINAL PREPARED FOR ISSUE DISCLAMER WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only DISK (CRF) FILED WITH: CD-ROM

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